IC'Alps Builds a Secure Infrastructure for Ultimate Protection of Customers' Data

Meylan, FRANCE – June 08, 2021 – In wake of the recently announced partnership with Tiempo Secure for silicon implementation of CC (Common Criteria) EAL5+ grade Secure Element IP, IC'Alps has deployed an infrastructure that complies with the highest security standards.

IC'Alps is a French semiconductor company specializing in the design and supply of exclusive and custom-made ASIC solutions (Application Specific Integrated Circuits). As part of the improvements, the company has set up a secure access management system including premises complementary access control, dedicated and secure design room, and an isolated and secure IT network. This revamp increases the security in IC'Alps' Head Office as demanded by security-sensitive customers or partners, requiring such a high level of data protection and associated traceability.

IC'Alps successfully passed CC audit of secure room and associated IT infrastructure commissioned by Tiempo Secure, audit performed by a security evaluation labs (ITSEF) licensed by the ANSSI. The audit states that the silicon physical implementation work entrusted by Tiempo Secure to IC'Alps passed the very strict requirements.

"Since the company was founded, IC'Alps has invested significant financial and human resources into IT. We are proud to pass this CC audit for this customer's project. This is an independent recognition of our commitment to providing the highest level of quality, data security and customer service", said Jean-Luc Triouleyre, CEO of IC'Alps.

In addition to ISO 13485 (medical) and EN 9100 (aeronautics) certifications, joining an elite group of companies providing a design environment complying with the highest security standards, will strengthen IC'Alps' business opportunities for silicon implementation of custom chips and hard macros targeted for security-sensitive markets such as, but not limited to, banking, defence, government and implantable medical devices.

ABOUT IC'ALPS

IC'Alps is your one-stop-shop ASIC partner. The company provides customers with a complete offering for Application Specific Integrated Circuits (ASIC) and Systems on Chip (SoC) development from circuit specification, mastering design in-house, up to the management of the entire production supply chain. From its technical centre in France (Grenoble area), IC'Alps supports multiple projects in the demanding medical, industrial, transport, IoT, and mil/aero sectors. The highly qualified engineering teams cover every expertise needed and have a long experience of on-demand analog, mixed-signal and digital integrated circuits on technologies from 0.35 μm down to 16 nm. Moreover, IC'Alps has a proven track record of success with sensor/MEMS AFE interfaces, low power consumption designs, high-resolution converters, signal processing, multiprocessors architectures, hardware accelerators, to name a few. Being a partner of major semiconductor foundries, IP providers, as well as packaging and test houses, IC'Alps is well placed to support customers with full life-cycle solutions. We are proud to be Arm® approved Design Partner, a member of X-FAB's design and supply chain partner network, as well as an Imec IC-link partner. These accreditations assure that we provide expert support and that we have the design expertise to create highly innovative ASIC designs. IC'Alps is ISO 9001, ISO 13485, EN 9100 certified, as well as Common Criteria ready. More information can be found at www.icalps.com